Fig. 1

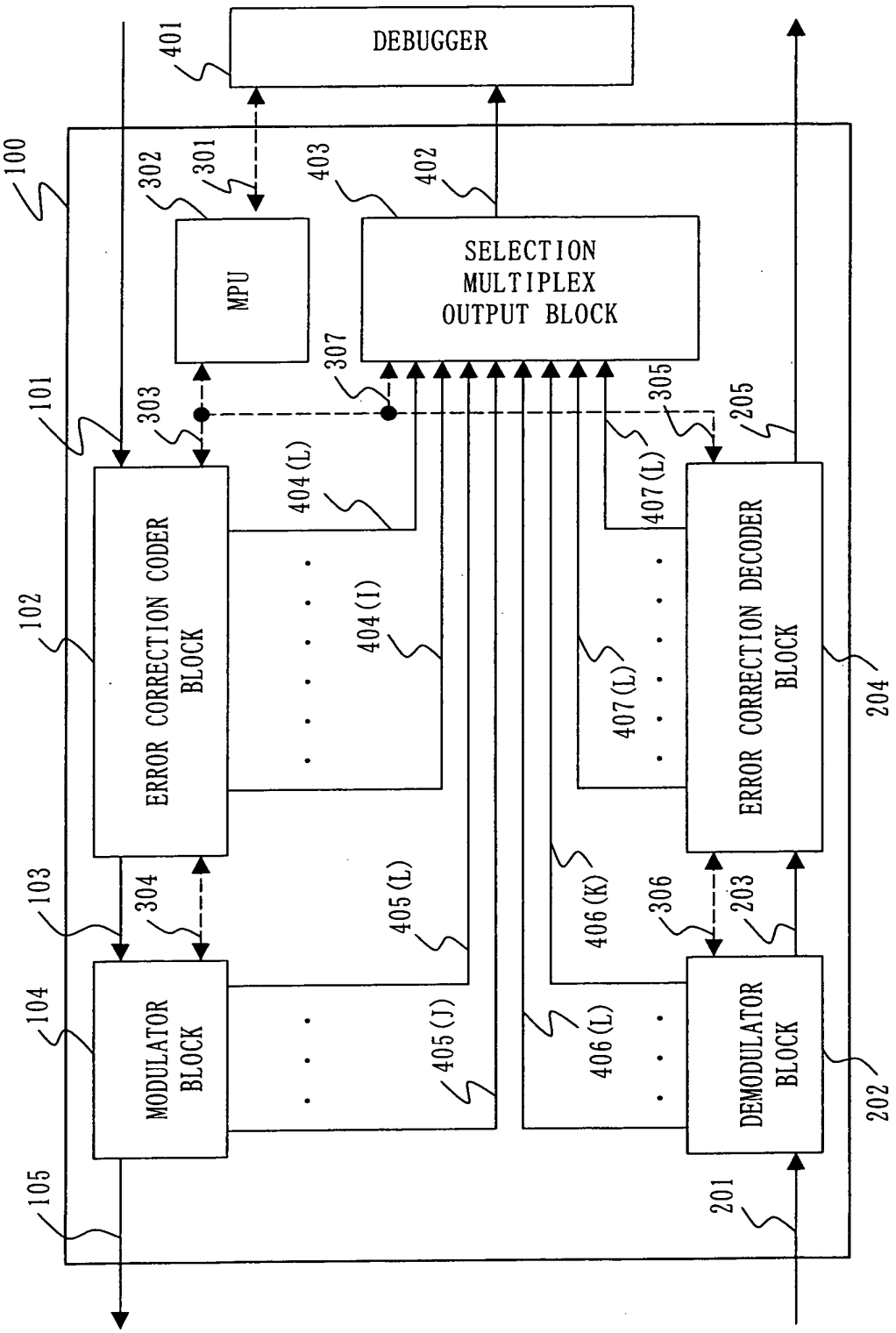

DEBUGGER
401
100
302
301
403
402
MPU
SELECTION
MULTIPLEX
OUTPUT BLOCK
307
305
101
303
205
404(L)
407(L)
102
ERROR CORRECTION CODER
BLOCK
404(1)
407(L)
ERROR CORRECTION DECODER
BLOCK
204
103
304
405(L)
406(K)
306
203
104
MODULATOR
BLOCK
405(J)
406(L)
DEMODULATOR
BLOCK
202
105
201

Fig. 2

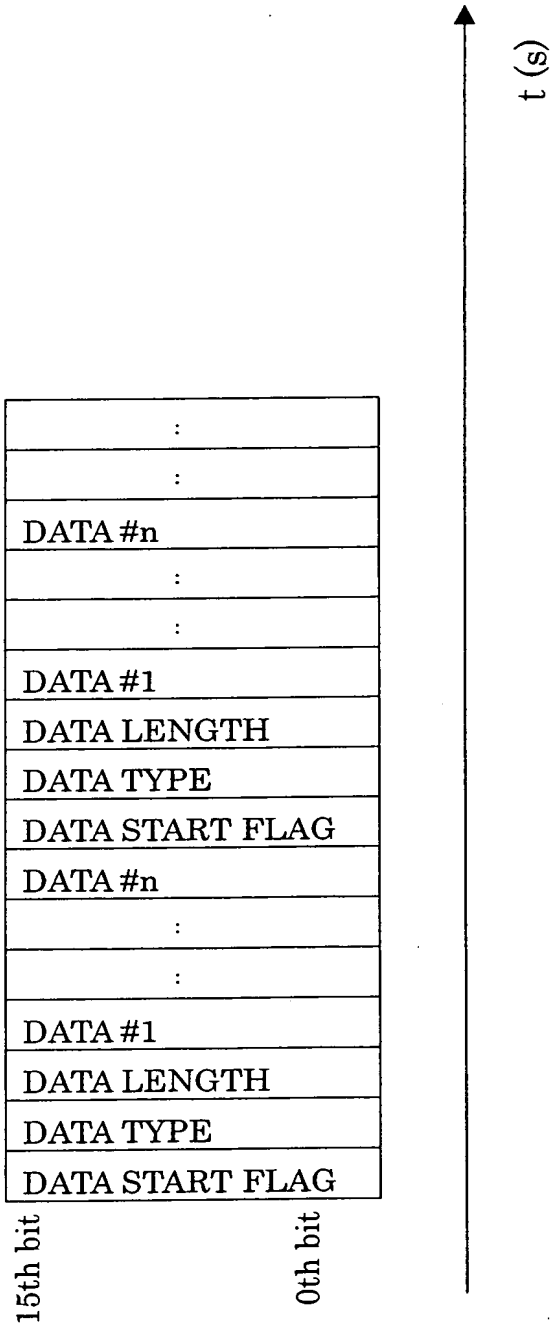

t (s)
:
:
DATA #n
:
:
DATA #1
DATA LENGTH
DATA TYPE
DATA START FLAG
DATA #n
:
:
DATA #1
DATA LENGTH
DATA TYPE
DATA START FLAG
15th bit
0th bit

Fig. 3

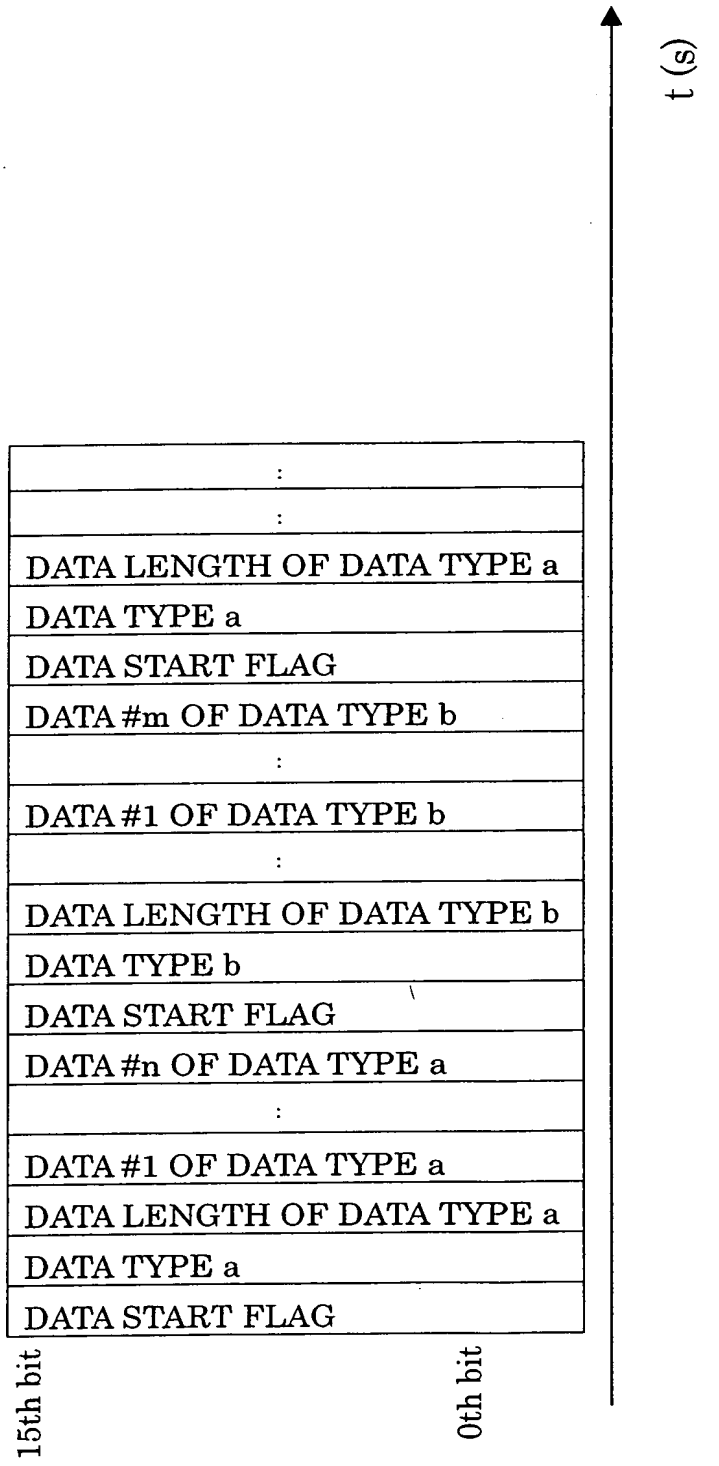

t (s)
:
:
DATA LENGTH OF DATA TYPE a
DATA TYPE a
DATA START FLAG
DATA #m OF DATA TYPE b
:
DATA #1 OF DATA TYPE b
:
DATA LENGTH OF DATA TYPE b
DATA TYPE b
DATA START FLAG
DATA #n OF DATA TYPE a
:
DATA #1 OF DATA TYPE a
DATA LENGTH OF DATA TYPE a
DATA TYPE a
DATA START FLAG
15th bit
0th bit

Fig. 4

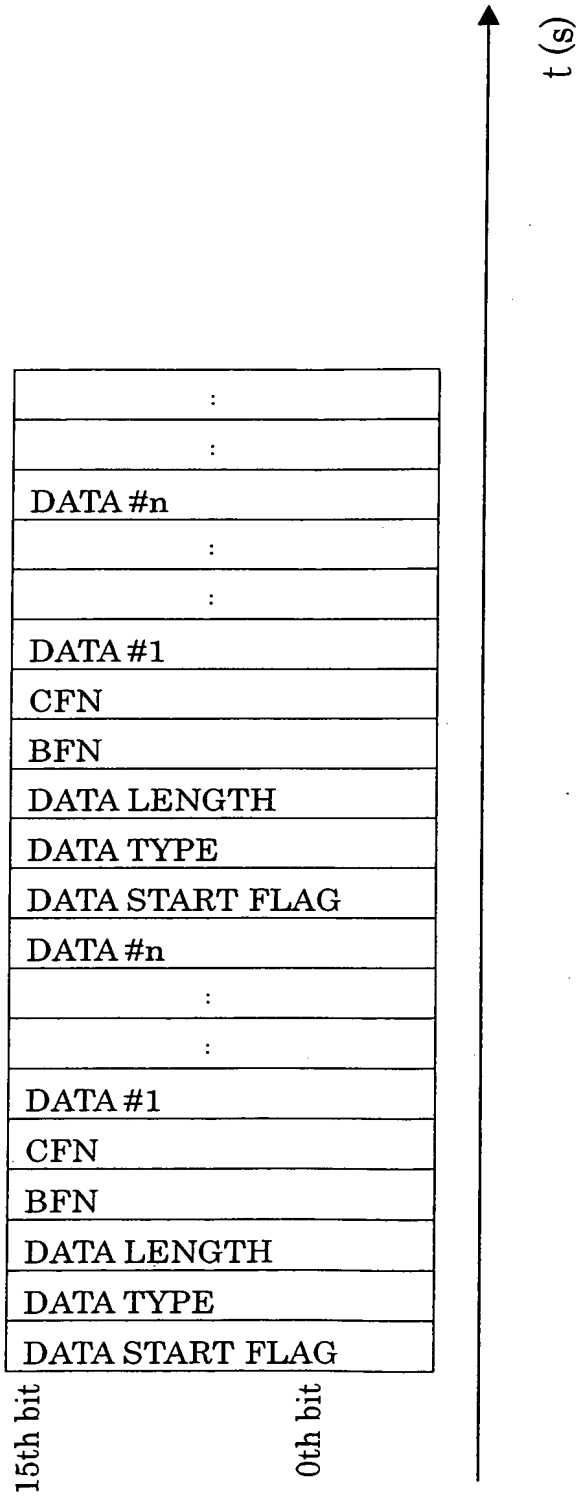

t (s)
:
:
DATA #n
:
:
DATA #1
CFN
BFN
DATA LENGTH
DATA TYPE
DATA START FLAG
DATA #n
:
:
DATA #1
CFN
BFN
DATA LENGTH
DATA TYPE
DATA START FLAG
15th bit
0th bit

Fig. 5

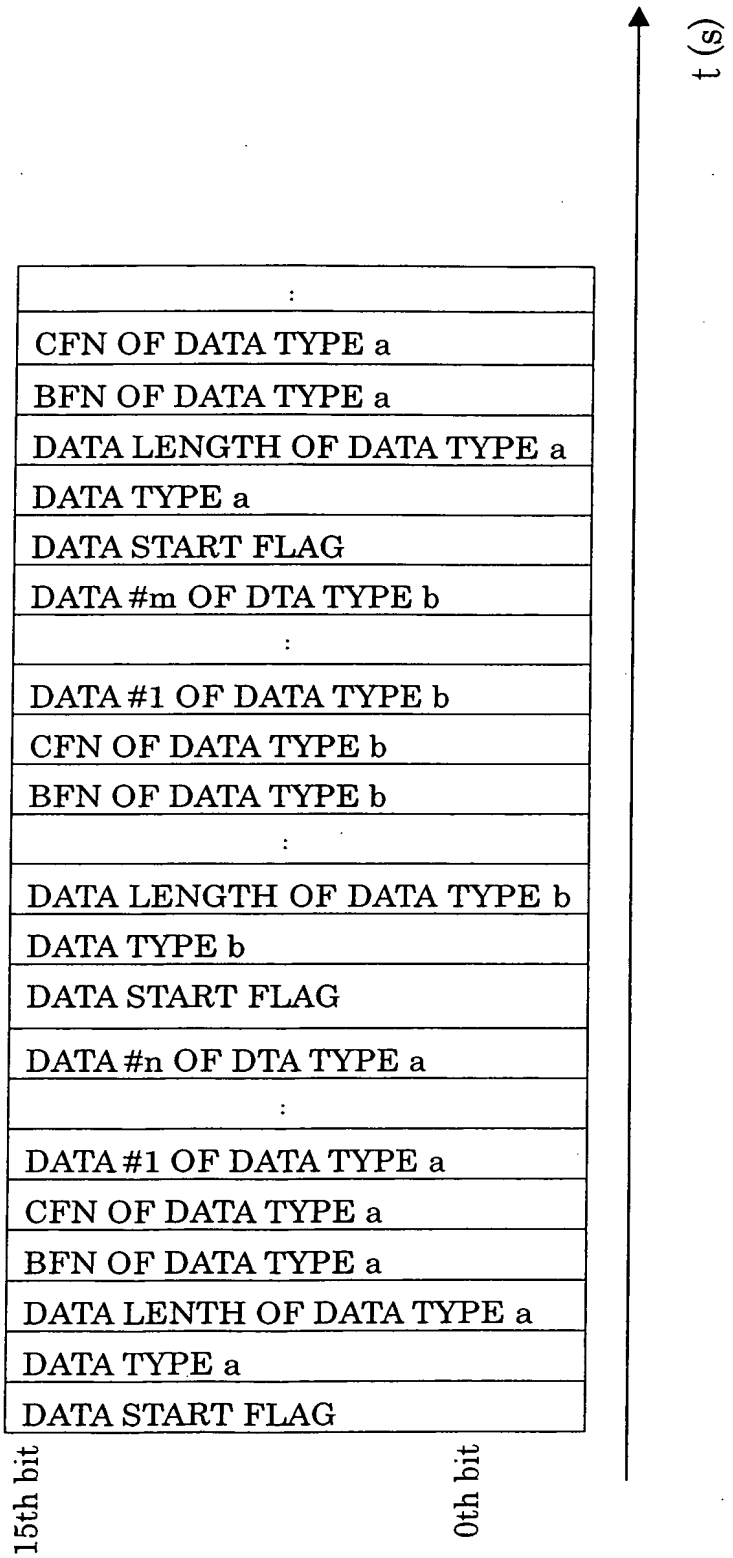

t (s)
:
CFN OF DATA TYPE a
BFN OF DATA TYPE a
DATA LENGTH OF DATA TYPE a
DATA TYPE a
DATA START FLAG
DATA #m OF DTA TYPE b
:
DATA #1 OF DATA TYPE b
CFN OF DATA TYPE b
BFN OF DATA TYPE b
:
DATA LENGTH OF DATA TYPE b
DATA TYPE b
DATA START FLAG
DATA #n OF DTA TYPE a
:
DATA #1 OF DATA TYPE a
CFN OF DATA TYPE a
BFN OF DATA TYPE a
DATA LENTH OF DATA TYPE a
DATA TYPE a
DATA START FLAG
15th bit
0th bit

Fig. 6

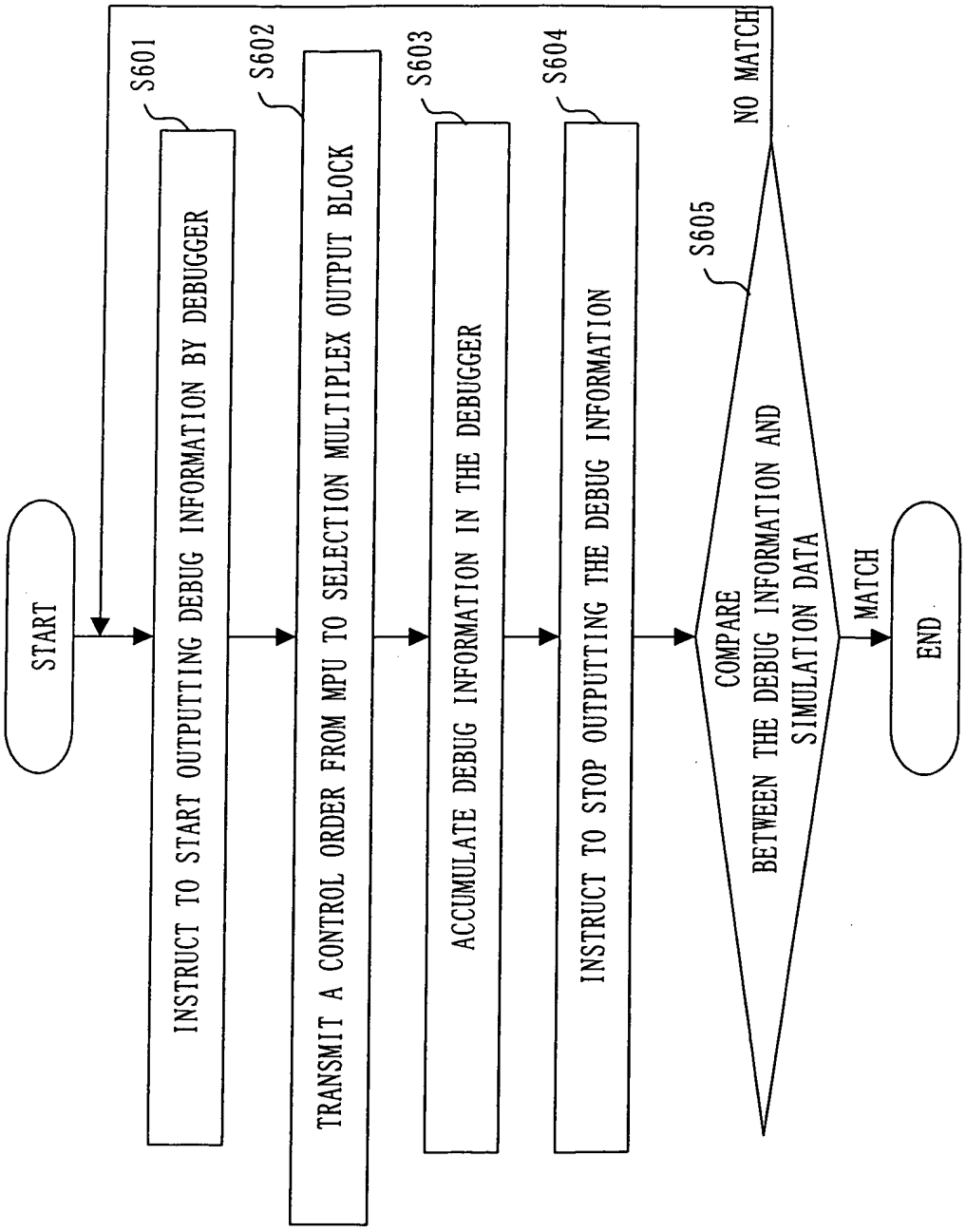
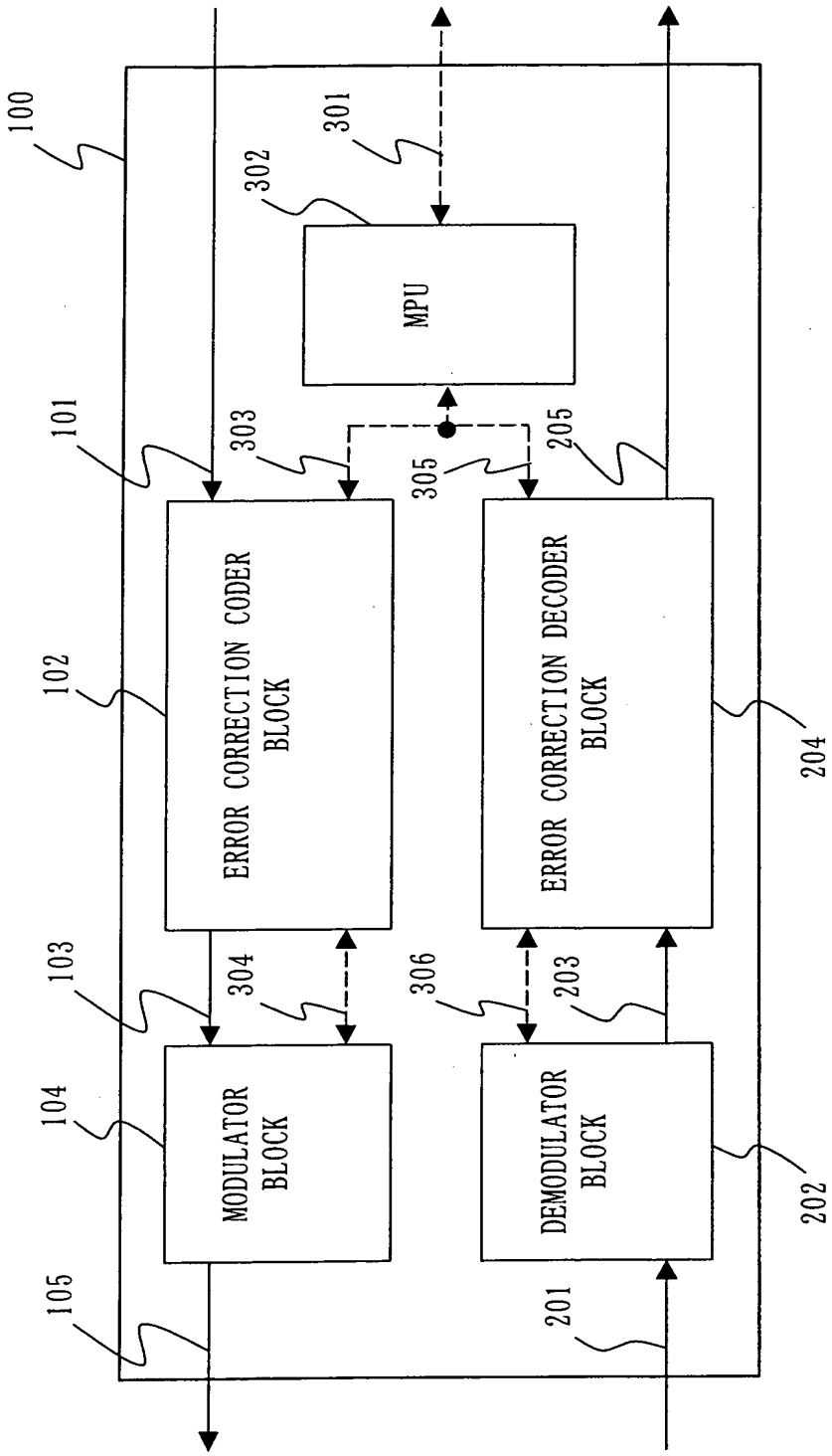

START
S601
INSTRUCT TO START OUTPUTTING DEBUG INFORMATION BY DEBUGGER
S602
TRANSMIT A CONTROL ORDER FROM MPU TO SELECTION MULTIPLEX OUTPUT BLOCK
S603
ACCUMULATE DEBUG INFORMATION IN THE DEBUGGER
S604
INSTRUCT TO STOP OUTPUTTING THE DEBUG INFORMATION
S605
COMPARE BETWEEN THE DEBUG INFORMATION AND SIMULATION DATA
NO MATCH
MATCH
END

Fig. 7


100
301
302
MPU
101
303
305
205
ERROR CORRECTION CODER BLOCK
ERROR CORRECTION DECODER BLOCK
102
204
103
304
306
203
MODULATOR BLOCK
DEMODULATOR BLOCK
104
202
105
201